iDAQ-731 iDAQ-751 iDAQ-763D

16-ch IDI and 16-ch Universal IDO iDAQ Module

48-ch TTL Digital I/O iDAQ Module

16-ch SSR Output iDAQ Module









iDAQ-751





iDAQ-763D

Load voltage

Load current

Peak load current

Response time

Isolation protection

Power-on output state Acquisition type Buffered output

Output impedance OFF-state leakage current

Specifications

Solid State Relay (SSR) Output



16 60 VDC max. 1.3 A max.@ 25 °C / 0.7 A max.@ 60 °C per channel 4 A (100 ms, 1 pulse)

Turn-on time 1.0 ms typ./1.3 ms

OFF state Static or buffered, software configurable

500 Hz max software configurable

Internal data buffer (FIFO) size 512 samples, each sample contains state

0.6 ms typ./0.8 ms

 $0.13~\Omega$ tvp./ $0.5~\Omega$ max

1 IIA max

Turn-off time

Update rate

of all channels

600 VRMS

Specifications

Isolated Digital Input

Channels Input type Input logic level (referenced to DICOM)

Input current draw

Input protection voltage Response time Debounce filter

Isolation protection Acquisition type

Buffered acquisition

Edge detection

Pattern match detection

Other feature

Channels Output type

Load voltage

Load current

Buffered output

General

Output impedance

Response time Isolation protection

Current limit protection

Flyback diode protection Power-on output state Acquisition type

Operating temperature

Storage temperature

Operating humidity Storage humidity

Timing signal output

Universal Isolated Digital Output

0 V ~ +3 V +10 V ~ +40 V 1 mA max. OFF state ON state OFF state ON state 3 mA max. +60 V max, -8 V min. 100 µs max

10 μs ~ 84 ms, software configurable 600 VRMS Instant or buffered, software

configurable

Sample rate

10 kHz max., software configurable Internal data buffer (FIFO) size 512 samples, each sample contains state of all channels Interrupt Rising edge, falling edge, or both edges, software configurable for each channel independently By port detection, each channel can

be enabled or disabled by software independently 1 kHz max.

Latch digital input states when interrupt occurs
From one of the digital input channels or digital input pattern match event, software configurable

Sink (NPN) or source (PNP), software

configurable per port (8-channel) +10 V ~ +40 V 350 mA max. per channel

Static or buffered, software

configurable
Update rate
10 kHz max., software configurable

Internal data buffer (FIFO) size

512 samples, each sample contains state of all channels

 0.2Ω max.

100 μs max 600 VRMS

Specifications

Channels

48, software configurable for input or

Digital Input

Input logic level

Input protection voltage

Pull-down resistor Response time

Acquisition type

Buffered acquisition

Interrupt

Logic high Logic low Working voltage -0.5V~6.5V 3.5 V min 1.5 V max. -0.25 V ~ 5.25 V

-0.5V +0.5V 50 kΩ 5 μs max. 5.12 μs ~ 84 ms, software configurable Instant or buffered, software configurable

Sample rate 10 kHz max software configurable

Internal data buffer (FIFO) size 256 samples, each sample contains state of all channels

Edge detection
Rising edge, falling edge, or both edges, software configurable for each channel independently

Pattern match detection

By port detection, each channel can be enabled or disabled by software independently Frequency 1 kHz max. Latch digital input states when interrupt

occurs
From one of the digital input channels

or pattern match output, software configurable

Digital Output

Trigger output to chassis

Outnut Ionic level

Other feature

Logic high 4.0 V min. @ 2 mA source, 5.2 V max. 0.3 V max. @ 2 mA Logic low sink One channel 5 mA max. All channels summed 64 mA max.

Output current

Response time Default output state

Acquisition type Buffered output

5 µs max. Logic low Static or buffered, software configurable Update rate

10 kHz max., software configurable Internal data buffer (FIFO) size 256 samples, each sample contains state of all channels

-20 °C to 60 °C (-4 °F to 140 °F) -40 °C to 70 °C (-40 °F to 158 °F) 10% to 90% RH, non-condensing 5% to 95% RH, non-condensing

General

Power consumption from chassis 1.05 W typ. / 1.5 W max. Dimensions $100 \times 80 \times 25$ mm $(3.94 \times 3.15 \times 0.98$

Dimensions

Operating temperature Storage temperature Operating humidity Storage humidity

Vibration

-20 °C to 60 °C (-4 °F to 140 °F) -40 °C to 70 °C (-40 °F to 158 °F) 10% to 90% RH, non-condensing Certification 5% to 95% RH, non-condensing

5Grms EMC: CE. FCC

Power consumption from chassis 425 mW typ./ 450 mW max. Dimensions $100\times80\times25 \text{ mm } (3.94\times3.15\times0.98$

Certification

Ordering Information

■ iDAQ-751-AE 48-ch TTL Digital I/O iDAQ module

5Grms

EMC: CE, FCC Safety: CB, UL

Optional Accessories

PCL-10162-1E

DB-62 Shielded Cable, 1m DB-62 Shielded Cable, 3m DB-62 Wiring Terminal, DIN-rail Mount

General Power consumption from chassis 225 mW typ./ 800 mW max. Dimensions $100\times80\times25 \text{ mm } (3.94\times3.15\times0.98$

Operating temperature

Storage temperature Operating humidity Storage humidity Vibration

Shock Certification

in.)
-20 °C to 60 °C (-4 °F to 140 °F)
-40 °C to 70 °C (-40 °F to 158 °F)
10% to 90% RH, non-condensing
5% to 95% RH, non-condensing

5Grms EMC: CE, FCC Safety: CB, UL

Ordering Information ■ iDAQ-763D-AE

16-ch SSR Output iDAQ module

Ordering Information

All product specifications are subject to change without notice.

■ iDAQ-731-AE

16-ch IDI and 16-ch Universal IDO iDAQ module